



## SSCE5V042N1

1-line Bidirectional Micro Packaged TVS Diodes for ESD Protection

### ● Description

The SSCE5V042N1 is designed with Punch-Through process TVS technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. The small size and high ESD surge protection make SSCE5V042N1 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

### ● Feature

- ✧ 100W peak pulse power ( $t_P = 8/20\mu s$ )
- ✧ DFN1006-2L Package
- ✧ Working voltage: 5V
- ✧ Low clamping voltage
- ✧ Low capacitance
- ✧ Low leakage current
- ✧ Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test
    - Air discharge:  $\pm 25kV$
    - Contact discharge:  $\pm 25kV$
  - IEC61000-4-5 (Lightning) 8A (8/20 $\mu s$ )

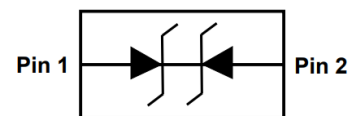
### ● Applications

- ✧ Cellular Handsets and Accessories
- ✧ Personal Digital Assistants
- ✧ Notebooks and Handhelds
- ✧ Portable Instrumentation
- ✧ Digital Cameras
- ✧ Peripherals
- ✧ Audio Players
- ✧ Keypads, Side Keys, LCD Displays

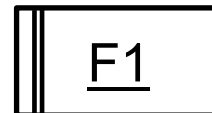
### ● PIN configuration



**DFN1006-2L (Bottom View)**



**Circuit Diagram**



**Marking**

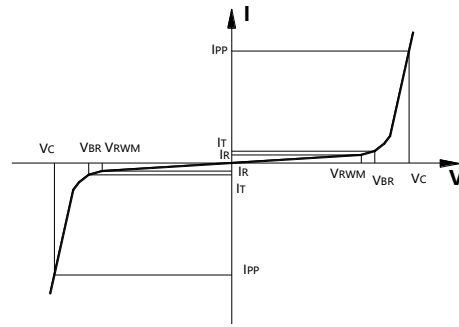
### ● Mechanical data

- ✧ Package: DFN1006-2L(1.0x0.6x0.5mm)
- ✧ Lead finish: 100% matte Sn (Tin)
- ✧ Device meets MSL 3 requirements
- ✧ Case Material: "Green" Molding Compound
- ✧ RoHS Compliant
- ✧ Pure tin plating: 7~17um
- ✧ Pin flatness:  $\leq 3mil$



## ● Electronic Parameter

Symbol	Parameter
$V_{RWM}$	Peak Reverse Working Voltage
$I_R$	Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$P_{PP}$	Peak Pulse Power
$C_J$	Junction Capacitance



## ● Absolute maximum rating @ $T_A=25^\circ\text{C}$

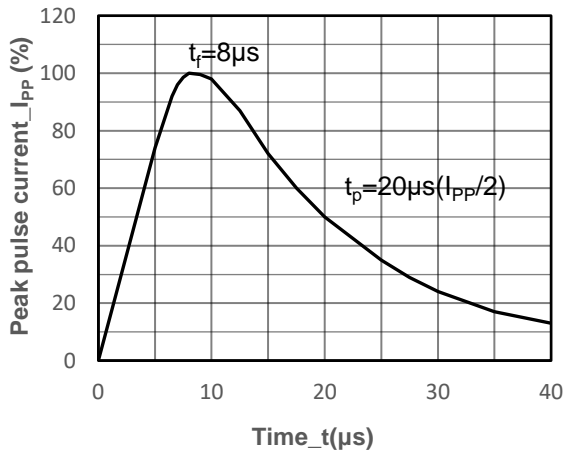
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 $\mu\text{s}$ )	$P_{PP}$	100	W
Peak Pulse Current (8/20 $\mu\text{s}$ )	$I_{PP}$	8	A
ESD Rating per IEC61000-4-2:		Contact	$\pm 25$
		Air	$\pm 25$
Storage Temperature	$T_{STG}$	-55/+150	$^\circ\text{C}$
Operating Temperature	$T_J$	-55/+125	$^\circ\text{C}$

## ● Electrical Characteristics @ $T_A=25^\circ\text{C}$

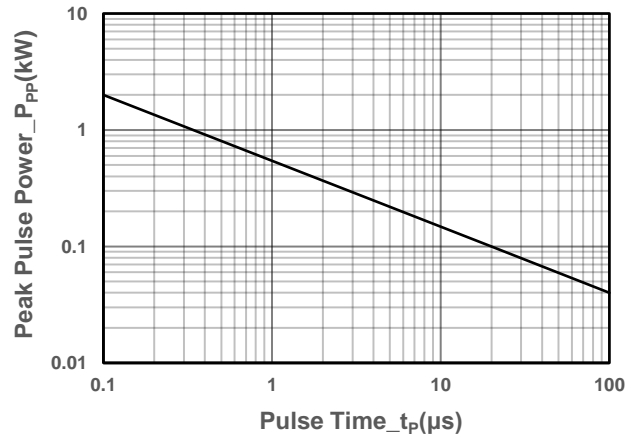
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Peak Reverse Working Voltage	$V_{RWM}$				5	V
Breakdown Voltage	$V_{BR}$	$I_T = 1\text{mA}$	5.6			V
Reverse Leakage Current	$I_R$	$V_{RWM} = 5\text{V}$			1	$\mu\text{A}$
Clamping Voltage	$V_C$	$I_{PP} = 1\text{A}, t_P = 8/20\mu\text{s}$			8	V
Clamping Voltage	$V_C$	$I_{PP} = 8\text{A}, t_P = 8/20\mu\text{s}$		10	13	V
Junction Capacitance	$C_J$	$V_R = 0\text{V}, f = 1\text{MHz}$		15	20	pF



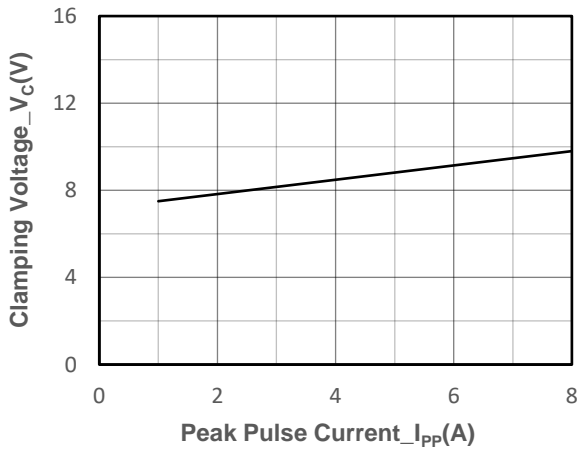
● Typical Performance Characteristics



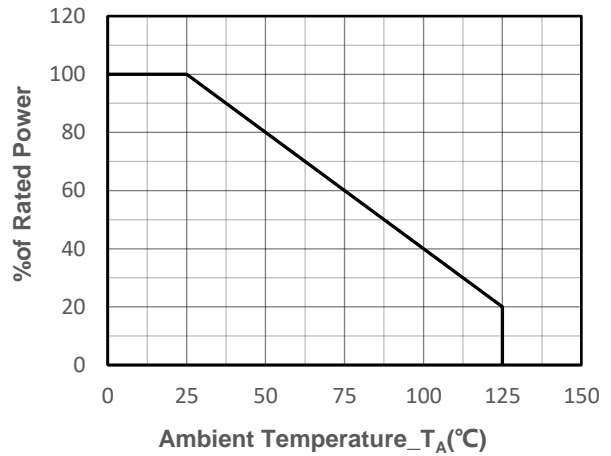
8/20µs Pulse Waveform



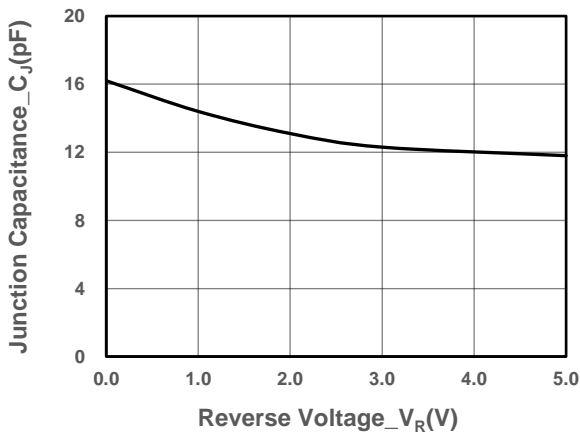
Peak Pulse Power vs. Pulse Time



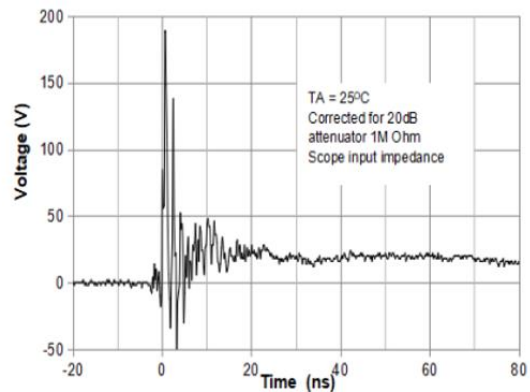
Clamping Voltage vs. Peak Pulse Current



Power derating vs. Ambient temperature



Junction Capacitance vs. Reverse Voltage



Note: Data is taken with a 10x attenuator  
ESD Clamping Voltage  
8kV Contact per IEC61000-4-2



## ● Package Information

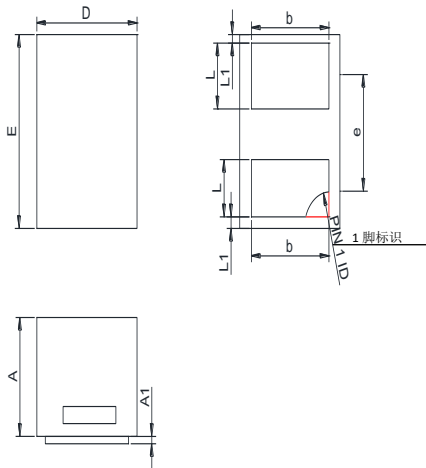
### Ordering Information

Device	Package	Qty per Reel	Reel Size
SSCE5V042N1	DFN1006-2L	10000	7 Inch

### Mechanical Data

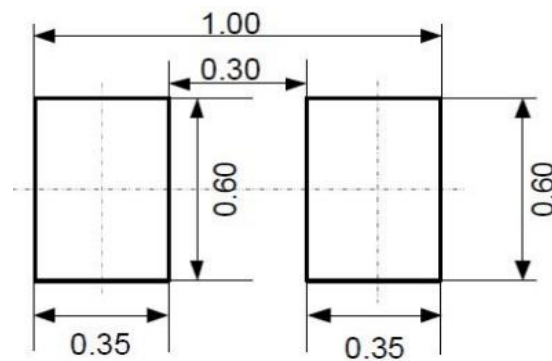
Case: DFN1006-2L

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters	
	Min	Max
A	0.45	0.55
A1	0.00	0.05
D	0.55	0.65
E	0.95	1.05
b	0.45	0.60
e	0.65TYP	
L	0.2	0.3
L1	0.05REF	

### Recommended Pad outline (Unit: mm)





- **History Version**

V1.0	First edition	2019-06-08
V2.0	Modify package size	2020-05-10
V3.0	Modify the company logo	2020-07-15
V3.1	Modify the product VC value and marking	2021-05-12
V3.2	Modify typical performance characteristics	2022-05-08
V3.3	Modify description and applications	2023-07-11

## DISCLAIMER

AFSEMI RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. AFSEMI DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICIENCE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

THE GRAPHS PROVIDED IN THIS DOCUMENT ARE STATISTICAL SUMMARIES BASED ON A LIMITED NUMBER OF SAMPLES AND ARE PROVIDED FOR INFORMATIONAL PURPOSE ONLY. THE PERFORMANCE CHARACTERISTICS LISTED IN THEM ARE NOT TESTED OR GUARANTEED. IN SOME GRAPHS, THE DATA PRESENTED MAY BE OUTSIDE THE SPECIFIED OPERATING RANGE (E.G. OUTSIDE SPECIFIED POWER SUPPLY RANGE) AND THEREFORE OUTSIDE THE WARRANTED RANGE.